



# P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

## Department of Electronics & Communication Engg.

**TIME - TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : IV Section: A Class Room: ECE306 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			5	Tea Break	3	6	4	Lunch Break	<-----7/8----->		
Tue			4		1	2	5		3	6	
Wed					1	5	3		Soft Skills		
Thu			2		<-----7/8----->				1	5	6T
Fri			6		3	4	2				
Sat			4		1	2	6				

- |   |   |
|---|---|
| 1. P15EC41-Engineering Mathematics-IV                               | 5. P15EC45-Microcontroller (NA)               |
| 2. P15EC42-Analog Communication (ST)                                | 6. P15EC46-Electromagnetic Field Theory (HSV) |
| 3. P15EC43-Power Electronics (SJ)                                   | 7. P15ECL47-Power Electronics Lab             |
| 4. P15EC44-Digital Signal Processing (MR)                           | 8. P15ECL48-Microcontroller Lab               |
| 9. P15HU49- Aptitude and Reasoning Development- Intermediate (ARDI) |   |

Time-Table Cordinator

HOD Dept. E&C Engg.



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**TIME - TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : IV Section: B Class Room:ECE307 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			3	Tea Break	2	5	6	Lunch Break	6T		
Tue			1		5	4	2		<-----7/8----->		
Wed			4		1	6	3		Soft Skills		
Thu			1		3	2	6		4		
Fri			5		<-----7/8----->				2	3	
Sat			6		1	5	4				

- |   |   |
|---|---|
| 1. P15EC41-Engineering Mathematics-IV                               | 5. P15EC45-Microcontroller(SSP)               |
| 2. P15EC42-Analog Communication (BPV)                               | 6. P15EC46-Electromagnetic Field Theory (GSH) |
| 3. P15EC43-Power Electronics(Dr.NMMG)                               | 7. P15ECL47-Power Electronics Lab             |
| 4. P15EC44-Digital Signal Processing (HRK)                          | 8. P15ECL48-Microcontroller Lab               |
| 9. P15HU49- Aptitude and Reasoning Development- Intermediate (ARDI) |   |

Time-Table Cordinator

HOD Dept. E&C Engg.



# P.E.S.College of Engineering, Mandya-571401

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## Department of Electronics & Communication Engg.

**TIME - TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : IV Section: C Class Room:ECE205 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			5	Tea Break	4	3	6	Lunch Break			
Tue			1		5	6	2		4	3	6T
Wed			1		<-----7/8----->				Soft Skills		
Thu			4		3	5	1		2	6	
Fri					2	4	3		<-----7/8----->		
Sat			6		1	5	2				

- |   |  |
|---|--|
| 1. P15EC41-Engineering Mathematics-IV                               | 5. P15EC45-Microcontroller (SS)              |
| 2. P15EC42-Analog Communication (MNS)                               | 6. P15EC46-Electromagnetic Field Theory(HSP) |
| 3. P15EC43-Power Electronics (Dr.MBP)                               | 7. P15ECL47-Power Electronics Lab            |
| 4. P15EC44-Digital Signal Processing (KN)                           | 8. P15ECL48-Microcontroller Lab              |
| 9. P15HU49- Aptitude and Reasoning Development- Intermediate (ARDI) |  |

Time-Table Cordinator

HOD Dept. E&C Engg.



# P.E.S.College of Engineering, Mandya-571401

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## Department of Electronics & Communication Engg.

**TIME - TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : VI Section: A Class Room:ECE303 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			6	Tea Break	1	4	3	Lunch Break	<-----7/8----->		
Tue			5		2	3	6		4	1	3T
Wed			6		<-----7/8----->				5		
Thu					5	4	1		2	3	
Fri			2		1	4	3		6	5	
Sat			Soft Skills		<b>Soft Skills</b>				<b>Soft Skills</b>		

- |  |  |
|--|--|
| 1. P15EC61- Digital Design Using Verilog HDL (MJA) | 6. Elective -III- P15EC662- Error Control Coding (MR)            |
| 2. P15EC62- Computer Communication Networks (UP)   | P15EC664- VLSI Testing and Verification (HSV)                    |
| 3. P15EC63- Microwaves and Antennas(BSN)           | 7. P15ECL67- Verilog HDL Lab                                     |
| 4. P15EC64- Digital Communication (HRK)            | 8. P15ECL68- Microwave and Digital Communication Lab             |
| 5. Elective-II                                     | 9. P15EC68- Mini Project   |
| P15EC651- Multimedia Communication (ANA)           | 10. P15EC610- Aptitude and Reasoning Development - EXPERT( ARDE) |

Time-Table Cordinator

HOD Dept. E&C Engg.



(An Autonomous Institution Under VTU. Belgaum)

**Department of Electronics & Communication Engg.**

**TIME – TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : VI Section: B Class Room:ECE304 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			6	Tea Break	2	3	5	Lunch Break	1	4	
Tue			3		1	2	6		<-----7/8----->		
Wed			6		5	4	2		1	3	
Thu					<-----7/8----->				4	5	3T
Fri			5		1	4	3		6	2	
Sat			Soft Skills		<i>Soft Skills</i>				<i>Soft Skills</i>		

- |   |  |
|---|--|
| 1. P15EC61- Digital Design Using Verilog HDL (Dr.MBP) | 6. Elective -III- P15EC662- Error Control Coding (MR)            |
| 2. P15EC62- Computer Communication Networks(M)        | P15EC664- VLSI Testing and Verification (KCS)                    |
| 3. P15EC63- Microwaves and Antennas (MNS)             | 7. P15ECL67- Verilog HDL Lab                                     |
| 4. P15EC64- Digital Communication (SSP)               | 8. P15ECL68- Microwave and Digital Communication Lab             |
| 5. Elective-II  | 9. P15EC68- Mini Project   |
| P15EC651- Multimedia Communication (RM)               | 10. P15EC610- Aptitude and Reasoning Development - EXPERT( ARDE) |

Time-Table Cordinator

HOD Dept. E&C Engg.



**P.E.S.College of Engineering, Mandya-571401**

(An Autonomous Institution Under VTU. Belgaum)

**Department of Electronics & Communication Engg.**

**TIME – TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : VI Section: C Class Room:ECE204 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			6	Tea Break	1	4	3	Lunch Break	2	5	
Tue			2		5	1	6		4	3	
Wed			6		2	4	5		<-----7/8----->		
Thu					2	5	1		3	4	3T
Fri			3		<-----7/8----->				6	1	
Sat			Soft Skills		<i>Soft Skills</i>				<i>Soft Skills</i>		

- |  |  |
|--|--|
| 1. P15EC61- Digital Design Using Verilog HDL(CL) | 6. Elective -III- P15EC662- Error Control Coding (MR)            |
| 2. P15EC62- Computer Communication Networks(M)   | P15EC664- VLSI Testing and Verification (HSV+KCS)                |
| 3. P15EC63- Microwaves and Antennas(BPV)         | 7. P15ECL67- Verilog HDL Lab                                     |
| 4. P15EC64- Digital Communication (ST)           | 8. P15ECL68- Microwave and Digital Communication Lab             |
| 5. Elective-II                                   | 9. P15EC68- Mini Project   |
| P15EC651- Multimedia Communication (RM)          | 10. P15EC610- Aptitude and Reasoning Development - EXPERT( ARDE) |

Time-Table Cordinator

HOD Dept. E&C Engg.



# P.E.S.College of Engineering, Mandya-571401

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## Department of Electronics & Communication Engg.

**TIME – TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : VIII Section: A Class Room:ECE204,205,306 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon					Project Work				Project Work		
Tue					Project Work				Project Work		
Wed			3 (306)		1 (205)	4 (205)	2 (205)				
Thu			4 (303)		2 (306)	3 (306)	1 (306)				
Fri			3 (205)		1 (204)	4 (204)	2 (204)				
Sat			4 (204)		2 (204)	3 (204)	1 (204)				

- |   |   |
|---|---|
| 1. P13EC81- Satellite Communication (NKM) | 2.P13EC82- Operations Research(KCS)     |
| 3. Elective-4                             | 4. Elective-5 )                         |
| P13EC832- ADHOC Wireless Networks (HSP)   | P13EC841- Biometrics (BSS)              |
| P13EC833- ASIC Design (NA)                | P13EC843- Wireless Sensor Networks (UP) |
| 5. P13EC85- Project Work                  | 6.P13EC86- Seminar                      |

Time-Table Cordinator

HOD Dept. E&C Engg.



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## Department of Electronics & Communication Engg.

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**B.E. Semester : VIII Section: B Class Room:ECE303,304,307 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon					Project Work				Project Work		
Tue					Project Work				Project Work		
Wed			3 (102)		2 (303)	4 (303)	1 (303)				
Thu			4 (304)		1 (304)	3 (304)	2 (304)				
Fri			3 (307)		2 (307)	4 (307)	1 (307)				
Sat			4 (303)		1 (303)	3 (303)	2 (303)				

- |   |  |
|---|--|
| 1. P13EC81- Satellite Communication (NKM) | 2.P13EC82- Operations Research(MS)     |
| 3. Elective-4                             | 4. Elective-5 )                        |
| P13EC832- ADHOC Wireless Networks (HSP)   | P13EC841- Biometrics (SS)              |
| P13EC833- ASIC Design (NA)                | P13EC843- Wireless Sensor Networks(UP) |
| 5. P13EC85- Project Work                  | 6.P13EC86- Seminar                     |

Time-Table Cordinator

HOD Dept. E&C Engg.



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**TIME – TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018 )**

**B.E. Semester : VIII Section: C Class Room:AEL102 Month : Jan Year:2018**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon				Tea Break	Project Work			Lunch Break	Project Work		
Tue					Project Work				Project Work		
Wed			3		1	4	2				
Thu			4		2	3	1				
Fri			3		1	4	2				
Sat			4		2	3	1				

1. P13EC81- Satellite Communication (ANA)

2.P13EC82- Operations Research(MS)

3. Elective-4

4. Elective-5 )

P13EC832- ADHOC Wireless Networks (HSP)

P13EC841- Biometrics (BSS+SS)

P13EC833- ASIC Design (NA)

P13EC843- Wireless Sensor Networks(UP)

5. P13EC85- Project Work

6.P13EC86- Seminar

Time-Table Cordinator

HOD Dept. E&C Engg.