

Department of Electronics and Communication

Basic Electronics – P18EC25

Topic : Introduction to Communication, Elements of Communication and AM

<https://youtu.be/XHIKFhBltGE%20>

Topic: Types of AM and its spectrum, AM circuit and AM detector circuit

<https://youtu.be/7TPi6gBvB0k%20>

Unit -3

<https://www.youtube.com/playlist?list=PLFIDlpSgvkaXEpjV4WNWR7boPRoXA1OJn>

Unit -4

Universal gate, NAND and NOR implementation - <https://youtu.be/gKXEOb35nDA>

Half Adder, Full Adder and Ripple carry adder - https://youtu.be/z_g82FVt1KQ

Multiplexer and Decoder - <https://youtu.be/qzD6vdozvg4>

Unit - 5

https://www.youtube.com/playlist?list=PL0u_zDtQRsGHGuufaFHRj116kMI3Y1cx

<https://www.youtube.com/playlist?list=PLFIDlpSgvkaUo4r9VRS6YGFRp4gT6lEdi>

Analog and Digital Communication – P18EC42

Unit-3

https://drive.google.com/file/d/1ev8Ke9ctk8fzIgliXepSYVBDwwuGas_4/view?usp=sharing

https://drive.google.com/file/d/1FH7jR4BHCUEgja_OwiF0tp1XRKKINFA0/view?usp=drive_web

https://drive.google.com/file/d/1SOpfSXUI6sXwdYVOiV2KKvZ9x7ZqmN/view?usp=drive_web

<https://www.youtube.com/watch?v=BO7RXvivnYc>

<https://www.youtube.com/watch?v=PdIYPU1AfTc>

<https://drive.google.com/open?id=1oAaKVmgPIIn7J2TpTBVh7Kbs0Xem1E7Im>

<https://www.youtube.com/watch?v=7JXkqSLc18g>

<https://drive.google.com/open?id=1anLYEKOVSkuUpMw7TEpVITX4DeaHaJ55>

Microcontroller – P18EC45

- Subroutine - <https://youtu.be/QjhNEIDq3fk>
- Storage for local variable - https://youtu.be/mYHWgJHB9_Y
- Passing parameter to Subroutine - https://youtu.be/CBSJzG6_-Iw
- Interrupts - <https://youtu.be/ONcfxEQnXNc>
- WatchDog Timer - <https://youtu.be/upKWLPF2eDA>
- Basic Timer1 and Real Time Clock - <https://youtu.be/6oWbue4sYXU>

TimerA- <https://youtu.be/0xsENbYN21s>

<https://youtu.be/Kk6pFr-k8Gc>

<https://youtu.be/1t1BPiJTbeI>

Computer Communication Networks – P17EC62

https://swayam.gov.in/nd1_noc20_cs23/preview

<https://nptel.ac.in/courses/106/105/106105183/>

Wireless Communication Technologies –P15EC81

Unit 5

https://www.youtube.com/watch?v=AbNyQj_sI4c

https://www.youtube.com/watch?v=2x3Ie6VZ_sg

<https://www.youtube.com/watch?v=tv5oXpEli-o>

Design of analog Mixed Mode VLSI Circuits - P18MECE21

Current Mirror.

- Basic current mirrors - https://youtu.be/G3ArPXT_tBk
- Cascade mirrors - <https://youtu.be/CpMomDoyGkA>
- Output Voltage Swing in Cascode Mirror - <https://youtu.be/yy1Ff14hbMI>
- Modification of Cascode mirror for low-voltage operation - <https://youtu.be/6enAGBtKhWA>
- Active current mirrors - https://youtu.be/Z6cDFNUP_5s
- Large Signal and Small Signal analysis of Active Current Mirror https://youtu.be/2zxO_DUrF3E
- Common Mode Properties - <https://youtu.be/Yrw5V3iyENk>

Operational Amplifiers:

- Performance Parameter - <https://youtu.be/rriCww15oWk>
- One Stage op-amp - <https://youtu.be/pSO508nLR1g>
- Two Stage OP-Amp - <https://youtu.be/qVyQtidATMk>
- Gain Boosting, Comparison - <https://youtu.be/uAcicpqOAqqc>

Digital design Using Verilog HDL-P18EC43

UNIT 3: Useful system task - <https://youtu.be/59bMklVxKi4>

Unit 3: Useful Modeling Techniques- <https://youtu.be/jmc8VK2o4D8>

Unit 3 : Switch level Modeling : <https://youtu.be/eAYzmq0gBhA>

Unit 3: Timing and Delay 1: <https://youtu.be/oZz7AcGq4Oc>

Unit 3: Timing and Delay 2: https://youtu.be/oH7278_JyF8

Unit 4: UDP-<https://www.youtube.com/watch?v=WdfTkf1vCws>

UNIT 4: Logic Synthesis with Verilog HDL 1: <https://youtu.be/5qHRCrPEHsc>

UNIT 4: Logic Synthesis with Verilog HDL 2: https://youtu.be/OrHUPomF_rQ

UNIT 5 : Design Partining: <https://youtu.be/7anVGQep2xM>

UNIT 5 : Advanced Verification Techniques: <https://youtu.be/kx78DScioe8>

UNIT 5: Verification of the gate Level : <https://youtu.be/4PCqjljICKI>