P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

Department of Electronics & Communication Engg.

TIME – TABLE ODD SEM: (Reopening: Ending :)

M.Tech Semester : I **Class Room:SeminarHall** **Month :** Year:2018

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			1		3	4	2				
Tue			**5**		3	1	4				
Wed				Tea Break	3	2	4	Lunch Break	1		
Thu					1	2	5		<----------6---------->		
Fri	<----------6---------->				4	2	5		<-------6-------->		
Sat					5	3					

1.P18MECE11-CMOS VLSI Design (KCS)

2.P18MECE12- Advanced Embedded System (Dr.KAR)

3.P18MECE13- SoC Design (Dr.KNM)

7. P18MECE17 – Seminar-I on Mini Project

4.Elective-I P18MECE142- Digital System Design Using Verilog (MJA)

5 .Elective-II P18MECE151- ASICDesign (M)
P18MECE152- Multi-Core Architecture (NKM)

6. P18MECE16 – Embedded System Lab