

P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

Department of Electronics & Communication Engg.

TIME – TABLE ODD SEM: (Reopening: - -2016 Ending - -20)

Semester: I M.Tech Branch: E&C Class Room: Month: Year:2016

Time Day	7.30 to 8.25	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON				T E A		5	2	L U N C H	4	3	
TUE					1	5	3		4	2	
WED			4	B R E A K	3	1	2	B R E A K			
THU			1		←-7-L1--→		5				
FRI					4	3	2		1	5	
SAT					←-7-L2--→						

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| 1.P15MECE11-CMOS VLSI Design (MJA) | 4. P15MECE143- Digital system design using Verilog (MJA) |
| 2.P15MECE12- Advanced Embedded System (Dr.KAR) | 5. P15MECE151- ASIC Design (AMJ) |
| 3.P15MECE13- SoC Design (Dr.KNM) | 6. P15MECE16- Seminar-I on Mini Project |
| | 7. P15MECE17 – VLSI Design Lab |

In charge of Time-table

HOD. of E&C Engg. Dept.

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TIME – TABLE ODD SEM: (Reopening: 25-07-2016 Ending -11-2016)

Semester: III M.Tech Branch: E&C Class Room: AEL102 Month: July Year:2016

Time Day	7.30 to 8.25	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON				T E A				L U N C H			
TUE											
WED				B R E A K		3	2	B R E A K	1		
THU					2	3	1				
FRI					1	2	3		←-----5-----→		
SAT			1		3	2					

- | | |
|---|---|
| 1.P15MECE31-VLSI Testing and Verification (SJ) | 4. P15MECE34- Industrial Training |
| 2.P15MECE32-(Elective-III)P15MECE323- Automotive Electronics (Dr.HSS) | 5. P15MECE35-Pedagogy/ Research Methodology |
| 3.P15MECE33-(Elective-IV) P15MECE332-Advances in VLSI Design (BSN) | 6. P15MECE36- Project Phase-I |

In charge of Time-table

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