



P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

Department of Electronics & Communication Engg.

TIME - TABLE EVEN SEM: (Reopening: 29-01-2018 Ending : 12 -05-2018)

M.Tech Semester : II

Class Room: AEL102

Month : Feb

Year:2018

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon					6	1	5		3	4	
Tue					5	2	3		1	4	
Wed			5		2	6	1		<-----7---NA-KN--->		
Thu					1	6	3		2	4	
Fri					6	3	2		5	4	
Sat					<-----7---M---CL--->						

1. P17MECE21- Design of Analog and Mixed Mode VLSI

2. P17MECE22- Low Power VLSI Design(S)

3. P17MECE23- Real Time Systems (Dr.HSS)

4. Elective-III P17MECE241- ARM

Processor(Dr.KAR)

5. Elective-IVP17MECE252-Design of VLSI System(MJA)

6. Elective-V P17MECE262- Advances in VLSI Design (BSN)

7. P17MECE27- VLSI Design Lab

Time-Table Cordinator

HOD Dept. E&C Engg.