



# P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

## Department of Electronics & Communication Engg.

TIME - TABLE ODD SEM: (Reopening: 03-10-2017 Ending : )

M.Tech Semester : I Class Room:SeminarHall Month : Oct Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon				Tea Break		5	6	Lunch Break	4	2	
Tue					5	4	1		3	2	
Wed		<-----7----->			3	4	5		6	1	
Thu					5	3	2		6	1	
Fri		<-----7----->			6	2	4		3	1	
Sat											

- 1.P17MECE11-CMOS VLSI Design (Dr.VS)
- 2.P17MECE12- Advanced Embedded System (Dr.KAR)
- 3.P17MECE13- SoC Design (Dr.KNM)
4. P17MECE14- Multi-core Architecture(NKM)
5. Elective-I P17MECE152- Digital System Design Using Verilog (MJA)
- 6.Elective-II P17MECE161- ASIC Design (NA)
7. P17MECE17 - Embedded System Lab

Time-Table Cordinator

HOD Dept. E&C Engg.



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TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )

M.Tech Semester : III Class Room:Seminar Hall Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon				Tea Break				Lunch Break			
Tue											
Wed											
Thu			3		1	2			Soft Skills		
Fri			2		3	1	2		3		
Sat			1		3	1	2				

- 1.P15MECE31-VLSI Testing and Verification (Sj)
- 2.P15MECE32-(Elective-III)P15MECE323- Automotive Electronics (Dr.HSS)
- 3.P15MECE33-(Elective-IV) P15MECE332-Advances in VLSI Design (BSN)
4. P15MECE34- Industrial Training
5. P15MECE35-Pedagogy/ Research Methodology
6. P15MECE36- Project Phase-I

Time-Table Cordinator

HOD Dept. E&C Engg.