



# P.E.S.College of Engineering, Mandya-571401

(An Autonomous Institution Under VTU. Belgaum)

## Department of Electronics & Communication Engg.

TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )

B.E. Semester : III Section: A Class Room:ECL305 Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			4		5	3	2		<-----7/8----->		
Tue					1	6	4		2	5	T6
Wed					4	1	6		Soft Skills		
Thu			5		<-----7/8----->	2	3		2	6	3
Fri					3	2	T5		6	1	
Sat			3		1	5	4				

1. P15MAT31- Engineering Mathematics -III
2. P15EC32- Analog Electronic Circuits (NMMG)
3. P15EC33- Digital Electronics Circuits (Mahesh)
4. P15EC34- Electronic Instrumentation (RM)
5. P15EC35 – Network Analysis & Synthesis(MR)
6. P15EC36 – Signals and Systems (MS)
7. P15ECL37- FET and Op-Amp Circuits Lab
8. P15ECL38- Digital Circuits Design Lab
9. P15HU39- \*\*Aptitude and Reasoning Development- BEGINNER (ARDB)

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TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )

B.E. Semester : III Section: B Class Room:ECL306 Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			2		4	6	5				
Tue			1		<-----7/8----->	6	3		6	5	3
Wed			3		1	2	4		Soft Skills		
Thu			1		2	4	6		3	T5	T6
Fri					5	4	3		<-----7/8----->		
Sat			5		1	2	6				

1. P15MAT31- Engineering Mathematics -III
2. P15EC32- Analog Electronic Circuits (NMMG)
3. P15EC33- Digital Electronics Circuits (Mahesh)
4. P15EC34- Electronic Instrumentation (RM)
5. P15EC35 – Network Analysis & Synthesis (BPV)
6. P15EC36 – Signals and Systems (MS)
7. P15ECL37- FET and Op-Amp Circuits Lab
8. P15ECL38- Digital Circuits Design Lab
9. P15HU39- \*\*Aptitude and Reasoning Development- BEGINNER (ARDB)

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TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )

B.E. Semester : III Section: C Class Room:AEL-102 Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
Mon			6	Tea Break	2	3	5	Lunch Break	4	T5		
Tue			1		2	3	4		6	5	T6	
Wed			1		<-----7/8----->				Soft Skills			
Thu			2		5	6	1		4	3		
Fri			6		<-----7/8----->				2			
Sat			5		1	4	3					

1. P15MAT31- Engineering Mathematics -III
2. P15EC32- Analog Electronic Circuits (HSV)
3. P15EC33- Digital Electronics Circuits (Dr.KAR)
4. P15EC34- Electronic Instrumentation (MJA)
5. P15EC35 - Network Analysis & Synthesis(MNS)
6. P15EC36 - Signals and Systems (HRK)
7. P15ECL37- FET and Op-Amp Circuits Lab
8. P15ECL38- Digital Circuits Design Lab
9. P15HU39- \*\*Aptitude and Reasoning Development- BEGINNER (ARDB)

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**TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )**

**B.E. Semester : V Section: A Class Room:ECL301 Month : July Year:2017**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			1	Tea Break	3	Soft Skills		Lunch Break	<-----7/8----->		
Tue			4		1	3	2				
Wed	<-----7/8----->				5	4	2		6	1	T3
Thu			5		4	2	6				
Fri			2		6	3	4		5		
Sat			6		3	1	5				

1.P15EC51- Optical Communication Systems (ANA)

2. P15EC52- Digital CMOS VLSI Design(HSV)

3. P15EC53- Information Theory and Coding (Dr.MBP)

4. P15EC54-Control Systems(BPV)

5. P15EC55- Foundation Elective: P15EC551- Innovation Entrepreneurship Management(Dr.BVN)

6. P15EC56- Elective -I P15EC561- DSP Processor & Application(BSS)

7. P15ECL57- Digital Signal Processing Lab

8. P15ECL58- Optical & Analog Communication Lab

9. Industry Visit & Interaction

10. Aptitude and Reasoning Development- Advanced(ARDA)

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**TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )**

**B.E. Semester : V Section: B Class Room:ECL302 Month : July Year:2017**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			4	Tea Break	2	Soft Skills		Lunch Break	1	3	T3
Tue					3	2	1		<-----7/8----->		
Wed			4		6	1	2		5		
Thu			6		3	4	5				
Fri	<-----7/8----->				5	4	2		6		
Sat			5		3	1	6				

1. P15EC51- Optical Communication Systems (ST)

2. P15EC52- Digital CMOS VLSI Design (BSN)

3. P15EC53- Information Theory and Coding (SSP)

4. P15EC54-Control Systems (KCS)

5. P15EC55- Foundation Elective: P15EC551- Innovation Entrepreneurship Management (Dr.BVN)

6. P15EC56- Elective -I P15EC561- DSP Processor & Application(HRK)

7. P15ECL57- Digital Signal Processing Lab

8. P15ECL58- Optical & Analog Communication Lab

9. Industry Visit & Interaction

10. Aptitude and Reasoning Development- Advanced(ARDA)

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## Department of Electronics & Communication Engg.

**TIME - TABLE ODD SEM: (Reopening: 24-07-2017 Ending : 25 -11-2017 )**

**B.E. Semester : V Sec: C Class Room:AEL102,ECL207,301,302,305 Month : July Year:2017**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
Mon				Tea Break		Soft Skills		Lunch Break	4 ECL305	2 ECL305	3 ECL305	
Tue			1 ECL302		<-----7/8----->				2 ECL302	T3 ECL302	4 ECL302	
Wed			4 ECL301		G1-5 G2-6	1 AEL102	3 AEL102		G1-6 G2-5	G1-6 G2-5	2 ECL302	
Thu			G1-5 G2-6		3 ECL305	1 ECL305	G1-6 G2-5		<-----7/8----->			
Fri					G1-6 G2-5	3 ECL207	4 ECL207		G1-5 G2-6	G1-5 G2-6		
Sat			G1-6 G2-5		2 ECL207	1 ECL207	G1-5 G2-6					

1. P15EC51- Optical Communication Systems (GSH)

2. P15EC52- Digital CMOS VLSI Design (KCS)

3. P15EC53- Information Theory and Coding (PU)

4. P15EC54-Control Systems (AMJ)

5. P15EC55- Foundation Elective: P15EC551-  
Innovation Entrepreneurship Management (Dr.BVN)

6. P15EC56- Elective -I P15EC561- DSP Processor &  
Application( BSS/HRK)

7. P15ECL57- Digital Signal Processing Lab

8. P15ECL58- Optical & Analog Communication Lab

9. Industry Visit & Interaction

10. Aptitude and Reasoning Development-  
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B.E. Semester : VII Section: A Class Room:ECL207 Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			3		5	4	1		6	2	
Tue			5		3	1	4		<-----7/8----->		
Wed			6		2	3	4		1		
Thu			Soft Skills		Soft Skills	5	6		2	4	1
Fri			5		<-----7/8----->				6	3	2
Sat											

1. P13EC71-GSM Communication and Networks (Dr.KNM)
2. P13EC72- Low Power VLSI Design (Dr.MBP)
3. P13EC73-Advanced Microcontrollers (BSS)
4. P13EC74-Embedded and Real Time Systems (Dr.HSS)
9. Industrial Visit

5. P13EC75-Elective (Group-B) P13EC753- Operating Systems ( ST)
6. P13EC76-Elective (Group-C) P13EC763- Multimedia Communication (SJ)
7. P13ECL77- Computer Communication Network and VLSI Lab
8. P13ECL78- Embedded and Real Time System Lab

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B.E. Semester : VII Section: B Class Room:ECL208 Month : July Year:2017

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00	Tea Break	10.30 to 11.25	11.25 to 12.20	12.20 to 1.15	Lunch Break	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon			2		6	4	1		5	3	
Tue			6		2	1	3				
Wed			5		<-----7/8----->				1	4	
Thu			Soft Skills		Soft Skills	5	6		2	4	3
Fri			6		1	3	4		5	2	
Sat					<-----7/8----->						

1. P13EC71-GSM Communication and Networks (MR)
2. P13EC72- Low Power VLSI Design (CL)
3. P13EC73-Advanced Microcontrollers (ANA)
4. P13EC74-Embedded and Real Time Systems (SS)
9. Industrial Visit

5. P13EC75-Elective (Group-B) P13EC753- Operating Systems (NKM)
6. P13EC76-Elective (Group-C) P13EC763- Multimedia Communication (MNS)
7. P13ECL77- Computer Communication Network and VLSI Lab
8. P13ECL78- Embedded and Real Time System Lab

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**B.E. Semester : VII Sec: C Class Room:AEL102,ECL208,301 Month : July Year:2017**

Time / Days	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
Mon				Tea Break	G1-5 G2-6	1 ECL301	3 ECL301	Lunch Break	G1-6 G2-5	4 ECL301	
Tue			G1-5 G2-6		<-----7/8----->				4 ECL301	2 ECL301	
Wed			G1-6 G2-5		2 ECL208	1 ECL208	3 ECL208				
Thu			<b>Soft Skills</b>		<b>Soft Skills</b>	G1-5 G2-5	G1-6 G2-6		<-----7/8----->		
Fri			G1-5 G2-6		4 AEL102	1 AEL102	2 AEL102		G1-6 G2-5	3 ECL301	
Sat			2 ECL208		1 ECL208	4 ECL208	3 ECL208				

1. P13EC71-GSM Communication and Networks (HSP)
2. P13EC72- Low Power VLSI Design (KN)
3. P13EC73-Advanced Microcontrollers (SSP)
4. P13EC74-Embedded and Real Time Systems (SS)
9. Industrial Visit

5. P13EC75-Elective (Group-B) P13EC753- Operating Systems (NKM/ST)
6. P13EC76-Elective (Group-C) P13EC763- Multimedia Communication (SJ/ MNS)
7. P13ECL77- Computer Communication Network and VLSI Lab
8. P13ECL78- Embedded and Real Time System Lab

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